

Docket No.: 056937-0089



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Kenichiro UDA

**RESPONSE UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE**

Application No.: 10/658,195

Filed: September 10, 2003

Customer No.: 20277  
Confirmation No.: 3242

Group Art Unit: 2825

Examiner: Yelena Rossoshek

Title: STRUCTURE OF POWER SUPPLY PATH UTILIZED IN DESIGN OF  
INTEGRATED CIRCUIT

**AMENDMENT UNDER 37 CFR 1.116**

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Final Office Action dated September 20, 2006, having a three-month shortened statutory period for response set to expire December 20, 2006, please amend the above-identified application as follows.

PLEASE ENTER  
JLR 01/18/2007